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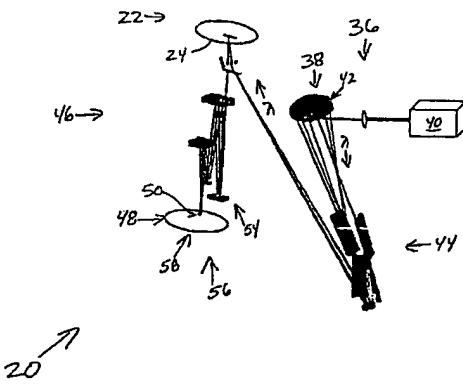
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(54) Title: EXTREME ULTRAVIOLET SOFT X-RAY PROJECTION LITHOGRAPHIC METHOD AND MASK DEVICES



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(57) Abstract: The present invention relates to reflective masks and their use for reflecting extreme ultraviolet soft x-ray photons to enable the use of extreme ultraviolet soft x-ray radiation projection lithographic methods and systems for producing integrated circuits and forming patterns with extremely small feature dimensions. The projection lithographic method includes providing an illumination sub-system for producing and directing an extreme ultraviolet soft x-ray radiation λ from an extreme ultraviolet soft x-ray source; providing a mask sub-system illuminated by the extreme ultraviolet soft x-ray radiation λ produced by the illumination sub-system and providing the mask sub-system includes providing a patterned reflective mask for forming a projected mask pattern when illuminated by radiation λ . Providing the patterned reflective mask includes providing a Ti doped high purity SiO_2 glass wafer with a patterned absorbing overlay overlaying the reflective multilayer coated Ti doped high purity SiO_2 glass defect free wafer surface that has an Ra roughness ≤ 0.15 nm. The method includes providing a projection sub-system and a print media subject wafer which has a radiation sensitive wafer surface wherein the projection sub-system projects the projected mask pattern from the patterned reflective mask onto the radiation sensitive wafer surface.

EXTREME ULTRAVIOLET SOFT X-RAY PROJECTION LITHOGRAPHIC METHOD AND MASK DEVICES

BACKGROUND OF THE INVENTION

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This application claims the benefit of U.S. Provisional Application, Serial Number 60/145,057, filed July 22, 1999 entitled Extreme Ultraviolet Soft X-Ray Projection Lithographic Method and Mask Devices, by Claude L. Davis, Robert Sabia and Harrie J. Stevens.

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U.S. Provisional Application, Serial Number 60/149,840, filed August 19, 1999 entitled Extreme Ultraviolet Soft X-Ray Projection Lithographic Method and Mask Devices, by Claude L. Davis, Kenneth E. Hrdina, Robert Sabia and Harrie J. Stevens.

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FIELD OF THE INVENTION

The present invention relates generally to projection lithographic methods and systems for producing integrated circuits and forming patterns with extremely small feature dimensions. The present invention relates particularly to extreme ultraviolet soft x-ray based lithography and reflective masks which reflect extreme ultraviolet soft x-ray radiation and form pattern images that are utilized to form circuit patterns. The present invention relates to reflective masks and their use for reflecting extreme ultraviolet soft x-ray photons to enable the use of extreme ultraviolet soft x-ray radiation for lithography that surpasses current optical lithography circuit feature dimension performance.

TECHNICAL BACKGROUND

The use of extreme ultraviolet soft x-ray radiation provides benefits in terms of achieving smaller feature dimensions but due to the nature of the radiation, it presents difficulties in terms of manipulating and directing such 5 wavelengths of radiation and has delayed the commercial manufacturing use of such radiation. Current optical lithography systems used in the manufacturing of integrated circuits have progressed towards shorter optical wavelengths of light, such as from 248 nm to 193 nm to 157nm, but the commercial use and adoption of extreme ultraviolet soft x-rays has been 10 hindered. Part of this slow progression to very short wavelengths of radiation such as in the 15 nm range, has been due to the lack of economically 15 manufacturable reflective mask wafers that can withstand the exposure to such radiation while maintaining a stable and high quality circuit pattern image. For the benefits of extreme ultraviolet soft x-rays to be utilized in the manufacturing of integrated circuits, there is a need for a stable glass wafer that allows for direct deposition of reflective coatings to the surface of the glass wafer.

As noted from United States Patent No. 5,698,113, current extreme ultraviolet soft x-ray lithographic systems are extremely expensive. United States Patent No. 5,698,113 tries to address such high costs by trying to 20 recover the surfaces of multilayer coated substrates by etching the multilayer reflective coatings from substrate surfaces of fused silica and ZERODUR type aluminosilicate glass-ceramics, even though such etching degrades the substrate surface.

The present invention provides for an economically manufactured mask 25 wafer that is stable, ready for direct coating and receptive to receiving multilayer reflective coatings and provides an improved extreme ultraviolet soft x-ray based projection lithography method/system. The present invention economically provides for improved mask wafer performance and stability 30 without the need to recycle the mask wafer surface which has been shown to reduce performance and the reflectivity of the mask. The present invention provides a stable high performance reflective mask with the reflective multilayer coating directly deposited on the finished glass surface, and avoids

costly and cumbersome manufacturing steps and intermediate layers between the glass substrate surface and the reflective multilayer coating.

SUMMARY OF THE INVENTION

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One aspect of the present invention is a projection lithographic method/system for producing integrated circuits with printed feature dimensions less than 100 nm that includes providing an illumination sub-system for producing and directing an extreme ultraviolet soft x-ray radiation λ from an extreme ultraviolet soft x-ray source. The method further includes providing a mask sub-system illuminated by the extreme ultraviolet soft x-ray radiation λ produced by the illumination sub-system and providing the mask sub-system includes providing a patterned reflective mask for forming a projected mask pattern when illuminated by radiation λ . Providing the patterned reflective mask includes providing a Ti doped high purity SiO_2 glass wafer with a patterned absorbing overlay overlaying the reflective multilayer coated Ti doped high purity SiO_2 glass defect free wafer surface that has an Ra roughness ≤ 0.15 nm. The method further includes providing a projection sub-system and an integrated circuit wafer which has a radiation sensitive wafer surface wherein the projection sub-system projects the projected mask pattern from the patterned reflective mask onto the radiation sensitive wafer surface.

In another aspect, the present invention includes a method of making a projection lithographic system and a method of projection lithography that includes providing an illumination sub-system which has an extreme ultraviolet soft x-ray source and providing a mask sub-system that has a mask receiving member and a reflective mask Ti doped high purity SiO_2 glass mask wafer with an unetched glass mask surface coated with a reflective multilayer coating having a reflectivity of at least 65% to extreme ultraviolet soft x-rays that is received in the mask receiving member. The method further includes providing a projection sub-system including a camera with a depth of focus $\geq 1 \mu\text{m}$ and a numerical aperture NA ≤ 0.1 ; providing a radiation sensitive print sub-system with a radiation sensitive print media; and aligning the illumination sub-system,

the mask sub-system, the projection sub-system, and the radiation sensitive print sub-system wherein the extreme ultraviolet soft x-ray source illuminates the reflective mask with extreme ultraviolet soft x-ray radiation and forms a printing pattern which is projected by the projection sub-system camera onto 5 said radiation sensitive print media.

The invention further includes a method of making a reflective extreme ultraviolet soft x-ray mask which includes the steps of: providing a Ti doped high purity SiO_2 glass preform having a preform surface and free of inclusions, finishing the preform surface into a planar mask wafer surface with an Ra 10 surface roughness ≤ 0.15 nm, and coating the finished planar mask wafer surface with a reflective multilayer coating to form a reflective mask surface having a reflectivity of at least 65% to extreme ultraviolet soft x-rays.

The invention also includes a reflective extreme ultraviolet soft x-ray mask wafer that comprises a Ti doped high purity SiO_2 inclusion-free glass 15 wafer having an unetched first polished planar face surface and an opposing second polished planar face surface, with the first surface free of printable surface defects that have a dimension greater than 80 nm and has a Ra roughness ≤ 0.15 nm.

The invention further includes a method of making a reflective extreme 20 ultraviolet soft x-ray mask wafer that has the steps of: providing a Ti doped high purity SiO_2 glass preform with a first preform surface and an opposing second preform surface, and is free of inclusions, and finishing the first preform surface into a planar mask wafer surface having an Ra roughness ≤ 0.15 nm.

Additional features and advantage of the invention will be set forth in the 25 detailed description which follows, and in part will be readily apparent to those skilled in the art from that description or recognized by practicing the invention as described herein, including the detailed description which follows, the claims, as well as the appended Figures.

It is to be understood that both the foregoing general description and the 30 following detailed description are merely exemplary of the invention, and are intended to provide an overview or framework for understanding the nature and character of the invention as it is claimed. The accompanying Figures are

included to provide a further understanding of the invention, and are incorporated in and constitute a part of this specification. The Figures illustrate various embodiments of the invention, and together with the description serve to explain the principals and operation of the invention.

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BRIEF DESCRIPTION OF THE DRAWINGS

FIG. 1 is a diagrammatic depiction of an embodiment of the invention;
10 FIG. 2 is a side cross-section of an embodiment of the invention;
FIG. 3 is a top view of an embodiment of the invention;
FIG. 4 is a diagrammatic depiction of an embodiment of the invention;
15 FIG. 5A is an AFM photomicrograph of an embodiment of the invention;
FIG. 5B is an AFM photomicrograph of an embodiment of the invention;
FIG. 6 a diagrammatic depiction of an embodiment of the invention;
FIG. 7A-7F are a manufacturing flow depiction of an embodiment of the
invention;
20 and FIG. 8 a plot of Thermal Expansion (y-axis) versus Temperature (x-
axis) for various wt. % TiO₂ levels in SiO₂ glass.

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DETAILED DESCRIPTION OF THE PREFERRED EMBODIMENTS

Reference will now be made in detail to the present preferred
embodiments of the invention, examples of which are illustrated in the
accompanying Figures. An exemplary embodiment of the projection
25 lithographic method/system of the present invention is shown in FIG. 1 and is
designated generally throughout by reference numeral 20.

In accordance with the invention, the present invention for a projection
lithographic method includes providing a mask sub-system illuminated by
extreme ultraviolet soft x-ray radiation λ produced by an illumination sub-
30 system, with the mask sub-system including a patterned reflective mask for
forming a projected mask pattern when illuminated by radiation λ with the
patterned reflective mask including a Ti doped high purity SiO₂ glass wafer with

a patterned absorbing overlay overlaying a reflective multilayer coated Ti doped high purity SiO₂ glass defect free wafer surface that has an Ra roughness ≤ 0.15 nm.

As embodied herein, and depicted in FIG. 1, projection lithographic 5 method/system 20 comprises mask sub-system 22 which includes patterned reflective mask 24. As shown in FIG. 2 and 3, patterned reflective mask 24 includes a Ti doped high purity SiO₂ glass wafer 26 with a patterned absorbing overlay 28 overlaying reflective multilayer coated Ti doped high purity SiO₂ glass defect free wafer surface 30. Reflective multilayer coated Ti doped high 10 purity SiO₂ glass defect free wafer surface 30 is comprised of reflective multilayer coating 34 on wafer surface 32, with reflective multilayer coating 34 preferably directly coating Ti doped high purity SiO₂ glass wafer surface 32. FIG. 4 shows the optical alignment of projection lithographic method/system 15 20. FIG. 4 shows the optical alignment of projection lithographic method/system 20.

15 The projection lithographic method for producing integrated circuits with printed feature dimensions less than 100 nm includes providing illumination sub-system 36 for producing and directing extreme ultraviolet soft x-ray radiation λ . Illumination sub-system 36 includes extreme ultraviolet soft x-ray source 38. In a preferred embodiment, illumination sub-system 36 includes a 20 1.064 μ m neodymium YAG laser 40 which produces a Xenon gas plasma 42 which outputs extreme ultraviolet soft x-ray radiation λ that is directed by condenser 44. Alternatively, extreme ultraviolet soft x-ray source 38 may comprise a synchrotron, discharge pumped x-ray lasers, an electron-beam driven radiation source device, or a radiation source based on high harmonic 25 generation based on femto-second laser pulses.

The projection lithographic method includes providing mask sub-system 22 which is illuminated by the extreme ultraviolet soft x-ray radiation λ produced by illumination sub-system 36. Providing mask sub-system 22 includes providing a patterned reflective mask 24 which forms a projected 30 mask pattern when illuminated by the radiation λ . Providing mask 24 includes providing Ti doped high purity SiO₂ glass wafer 26 which has a patterned absorbing overlay 28 overlaying reflective multilayer coated Ti doped high

purity SiO_2 glass defect free wafer surface 30. Ti doped high purity SiO_2 glass wafer surface 30 has an Ra roughness ≤ 0.15 nm. Ti doped high purity SiO_2 glass wafer 26 is free of defect inclusions and is a noncrystalline amorphous homogeneous glass solid with wafer surface 30 having a very smooth planar 5 surface that has an Ra roughness ≤ 0.15 nm and inhibits scattering of radiation λ that is to be reflected by the mask. Such an Ra roughness is achieved by finishing the surface in accordance with the invention and is measured using Atomic Force Microscopy (AFM). FIG. 5A – 5B are AFM photomicrographs of a Ti doped high purity SiO_2 glass wafer surface of the invention which has an 10 Ra roughness ≤ 0.15 as determined by AFM measurements.

The method includes providing a projection sub-system 46 and an integrated circuit wafer 48 with a radiation sensitive wafer surface 50, wherein projection sub-system 46 projects the projected mask pattern from mask 24 onto radiation sensitive wafer surface 50. Preferably projection sub-system 46 15 is a series of four mirrors as shown in FIG. 1 and 4 which reduce the size of the mask pattern and project the reduced pattern image onto wafer surface 50, with a 4x reduction power.

In a preferred embodiment, extreme ultraviolet soft x-ray radiation λ is in the range from about 5 nm to about 15 nm, and most preferably illumination 20 sub-system 36 directs radiation centered about 13.4 nm to reflective mask 24 that has a reflectivity of at least 65% at 13.4 nm.

The provided Ti doped high purity SiO_2 glass wafer 26 is defect free in that the glass body is free of bulk glass defects and is free of inclusion, including glass voids and gas filled voids in the glass, and is particularly free of 25 any such defects or imperfections that have a dimension greater than 80 nm. In a particularly preferred embodiment, glass wafer surface 32 is an unetched glass surface that has been finished with polishing to a planar surface Ra roughness ≤ 0.15 nm. Wafer 26 Ti doped SiO_2 glass is substantially non-transmissive to extreme ultraviolet soft x-ray radiation λ and the reflectivity of 30 reflective coated wafer surface 30 and the ultra-low roughness of wafer surface 32 is utilized in the invention to inhibit scattering of illumination radiation and provide an ultra-stable high quality image on wafer surface 50 during the

projection lithography process. The provided Ti doped high purity SiO_2 glass is preferably chlorine-free and preferably has an impurity level of less than 10 ppb of alkali and alkaline earth metals. Preferably the Ti doped high purity SiO_2 glass contains from 5 to 10 wt. % TiO_2 and has a coefficient of thermal expansion in the range from +30 ppb to -30 ppb at 20° C., and more preferably in the range from +10 ppb to -10 ppb at 20° C. Preferably wafer 26 has a variation in coefficient of thermal expansion throughout the wafer that is less than or equal to 15 ppb. Wafer 26 preferably has a thermal conductivity $K \leq 1.40 \text{ w/(m.}^\circ\text{C.)}$ at 25° C., more preferably in the range from 1.25 to 1.38, and most preferably about 1.31.

During projection lithography, wafer 26 is heated by the illumination of radiation λ , and even with such heating of the wafer the dimensions of the patterned absorbing overlay is substantially unaffected and changes of the projected image are inhibited and the quality of the projected image is maintained. In the inventive method, the Ti doped high purity SiO_2 glass wafer 26 is heated to an operating temperature by radiation λ and preferably the glass wafer has an adjusted Ti dopant level adjusted such that the glass wafer has a coefficient of thermal expansion centered about 0 at such operating temperature. With such a thermal conductivity and coefficient of thermal expansion, wafer 26 provides appropriate operation and stability, and provides a highly reliable and economic lithographic method/system in which mask subsystem 22 does not require cooling. In a preferred embodiment, mask 24 and wafer 26 are not actively cooled and are free of a cooling system such as circulating cooling fluids, thermal electric coolers, or other means for removing heat build up.

The provided Ti doped high purity SiO_2 glass defect free wafer surface 32 has a finished planar surface, with the finished planar surface free of printable surface imperfections that have a dimension greater than 80 nm. Preferably the finished planar surface is free of printable surface imperfections that have a dimension greater than 1/5 of the smallest printed feature dimension on wafer surface 50, such that the finished planar surface of the mask does not pollute the image printed on wafer surface 50. The reflective

multilayer coated Ti doped high purity SiO_2 glass wafer surface 30 preferably reflects at least 65%, and more preferably reflects at least 70%, of the radiation λ illuminating the reflective multilayer coating. In a preferred embodiment wafer surface 32 is unetched and free of an intermediate barrier layer or release layer so that multilayer reflective coating 34 is directly bonded thereto.

5 The invention further comprises a method of making a projection lithographic system and the method of projecting lithographic patterns that includes providing an illumination sub-system 36 with an extreme ultraviolet soft x-ray source 38 and providing a mask sub-system 22 with a mask receiving member 52 and a reflective mask Ti doped high purity SiO_2 glass mask wafer 24 with an unetched glass mask wafer surface 32 with an Ra roughness ≤ 0.15 nm coated with a reflective multilayer coating 34 having a reflectivity of at least 65% to extreme ultraviolet soft x-rays received in mask receiving member 52. The method further includes providing a projection sub-
10 system 46 with a camera 54 having a depth of focus ≥ 1 μm and a numerical aperture NA ≤ 0.1 ; providing a radiation sensitive print sub-system 56 with a radiation sensitive print media 58; and aligning illumination sub-system 36, mask sub-system 22, projection sub-system 46, and radiation sensitive print sub-system 56 such that extreme ultraviolet soft x-ray source 38 illuminate
15 reflective mask 24 with extreme ultraviolet soft x-ray radiation and reflective mask 24 reflects the radiation and forms a printing pattern of overlay 28 which is projected and reduced by projection sub-system camera 54 onto radiation sensitive print media 58.
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25 The method includes providing a reflective mask 24 with a Ti doped high purity SiO_2 glass mask wafer 26 free of inclusions and wafer surface defects that are printable on radiation sensitive print media 58. Providing mask sub-system 22 including a reflective mask 24, includes providing a Ti doped high purity SiO_2 glass mask wafer preform having a preform surface and free of inclusions, and finishing the preform surface into a planar mask wafer surface
30 having an Ra roughness ≤ 0.15 nm in accordance with the invention.

35 The method preferably includes determining an operating temperature of reflective mask 24 when illuminated by illumination sub-system 36 during

operation of system 20 and providing a mask sub-system 22 with a reflective mask 24 includes providing a Ti doped high purity SiO_2 glass mask wafer having a coefficient of thermal expansion at the determined operating temperature centered about 0. Such tuning of the coefficient of thermal expansion is done by controlled variations of Ti dopant content in the SiO_2 glass as demonstrated by FIG. 8 which shows the thermal expansion characteristics for high purity SiO_2 glasses with TiO_2 wt. % from 0 to 9.0 wt. % TiO_2 .

In a further embodiment of the method the Ti doped high purity SiO_2 glass mask wafer is heated to a raised temperature range by the extreme ultraviolet soft x-ray radiation and the Ti doped high purity SiO_2 glass has a coefficient of thermal expansion for the raised temperature range that is less than 10 ppb and greater than -10 ppb. Preferably the method includes providing a mask 24 and wafer 26 that is a thermal insulator (low thermal conductivity) with a thermal conductivity $K \leq 1.40 \text{ w}/(\text{m.}^\circ \text{C.})$, preferably in the range from 1.25 to 1.38, most preferably about 1.31, and allowing wafer 26 to heat up without actively cooling wafer 26.

The invention further comprises a method of making a reflective extreme ultraviolet soft x-ray projection lithography mask 24 that includes providing a Ti doped high purity SiO_2 glass preform having a preform surface and free of inclusions, finishing the preform surface into a planar mask wafer surface 32 having an Ra roughness $\leq 0.15 \text{ nm}$, and coating the finished planar mask wafer surface having a Ra roughness $\leq 0.15 \text{ nm}$ with a reflective multilayer coating 34 to form a reflective mask surface 30 having a reflectivity of at least 65% to extreme ultraviolet soft x-rays. The method further includes forming a patterned absorbing overlay 28 on reflective mask surface 30. Coating with a reflective multilayer coating preferably includes forming alternating smooth thin layers ($\leq 4 \text{ nm}$ thick) of a first element and a second element, such as Mo/Si or Mo/Be.

The alternating layers provide a soft x-ray extreme ultraviolet reflectivity peak, preferably centered about 13.4 nm. Such an alternating multilayer reflective coating functions similarly to a quarter wave stack, with the thickness

of the layers optimized for constructive interference of the photons reflected at each interface and minimal absorption so a large number of interfaces contribute to the high reflectance of the coating. Preferably layer to layer variation in thickness is within 0.01 nm. In a preferred embodiment, the coating 5 is 81 alternating layers of Mo and Si with Mo thickness about 2.8 nm and the Si thickness about 4.0 nm. With proper deposition conditions, reflectances of 68% or greater at 13.4 nm are achievable with such alternating layers. The 81 alternating layers of Mo and Si are preferably capped with a capping layer to prevent oxidation of the Mo on exposure to normal atmospheres, with a 10 preferred capping layer being a 4 nm thick Si layer. The reflective coating 34 of mask 24 is then patterned using wafer processing steps to form a pattern in an absorbing layer deposited on top of the reflective coating.

The absorbing layer is comprised of an absorbing element such as Al, Ti or other soft x-ray extreme ultraviolet absorbing elements and is patterned with 15 wafer processing steps such as optical exposure or e-beam direct write steps to form patterned absorbing layer 28. In a preferred embodiment, coating with a reflective multilayer coating and forming a patterned absorbing overlay includes, multilayer deposition on the Ti doped high purity SiO₂ glass wafer surface 32 which has an Ra roughness \leq 0.15 nm, then buffer layer deposition 20 on top of multilayer reflective coating, then absorber deposition on top of buffer layer, then pattern generation lithography, then pattern transfer into absorber, and then buffer layer etch removal to result in reflective coating with patterned absorber layer, with the reflective coating having a reflectivity of at least 65% to extreme ultraviolet soft x-rays, preferably centered about 13.4 nm.

25 In the preferred embodiment, the reflective multilayer coating is directly deposited onto the Ti doped high purity SiO₂ glass wafer surface. The finished surface and the Ti doped high purity SiO₂ glass properties provide for directly depositing and bonding between the glass surface and the reflective multilayer coating without the complications of indirect coating and without the need for 30 additional treatments of a substrate surface such as smoothing coating processes used with glass-ceramic crystalline containing substrate materials and remove the need for intermediate layers between the glass surface and

the reflective multilayer coating. A superior and stable reflective mask is achieved with the direct bonding contact between the finished Ti doped high purity SiO_2 glass surface and the multilayer reflective coating.

Preferably providing a Ti doped high purity SiO_2 glass preform as shown in FIG. 6, includes providing a high purity Si containing feedstock 114 and a high purity Ti containing feedstock 126, delivering high purity Si containing feedstock 114 and high purity Ti containing feedstock 126 to a conversion site 100, converting the delivered feedstocks into Ti doped SiO_2 soot 101, depositing soot 101 in a revolving zircon collection cup 142 of refractory zircon furnace 140 onto the upper glass surface of hot Ti doped high purity SiO_2 glass body 144, concurrently with the soot deposition consolidating the Ti doped SiO_2 soot into an inclusion free homogeneous Ti doped high purity SiO_2 glass body 144 and forming glass body 144 into a Ti doped high purity SiO_2 glass preform. Preferably providing high purity Si containing feedstock 114 and high purity Ti containing feedstock 126 includes providing a chlorine-free high purity Si containing feedstock and providing a chlorine-free high purity Ti containing feedstock, converting the chlorine-free feedstocks into a chlorine-free Ti doped SiO_2 soot and consolidating the soot into a chlorine-free Ti doped SiO_2 glass. Preferably the Si feedstock is a siloxane, preferably a polymethylsiloxane, more preferably a polymethylcyclotetrasiloxane, and most preferably high purity octamethylcyclotetrasiloxane (Si feedstock comprised of at least 99% octamethylcyclotetrasiloxane). Preferably the Ti feedstock is a titanium alkoxide, and more preferably titanium isopropoxide $[\text{Ti}(\text{OPri})_4]$, preferably with Ti feedstock comprised of at least 99% titanium isopropoxide. A nitrogen inert carrier gas 116 is bubbled through feedstocks 114 and 126, and a nitrogen inert carrier gas 118 is added to the Si feedstock vapor/carrier gas mixture and the Ti feedstock vapor/carrier gas mixture to prevent saturation and facilitate delivery of the feedstocks to conversion site 100, through distribution systems 120 and manifold 122. Preferably the Si feedstock is mixed with the Ti feedstock in manifold 122 to form a homogeneous gaseous Ti doped SiO_2 glass precursor mixture which is delivered through conduits 134 to conversion site burners 136 mounted in the upper portion 138 of furnace 140 which

produce conversion site burner flames 137, so that the feedstock mixture is converted into Ti doped SiO_2 soot 101 and then homogeneous Ti doped SiO_2 glass 144. The weight percent dopant content of TiO_2 in the SiO_2 glass can be adjusted by changing the amount of Ti feedstock delivered to conversion site 100 that is incorporated into soot 101 and glass 144. In a preferred method the Ti dopant weight percent level of glass 144 and preform 60 is adjusted so that wafer 26 has a coefficient of thermal expansion centered about 0 at an operating temperature of the mask incorporating wafer 26. In accordance with FIG. 8, adjustments to the wt. % TiO_2 in the high purity SiO_2 glass adjust the thermal expansion characteristics of the resultant glass wafer. Preferably the Ti dopant weight percent level of the glass is adjusted within the range from about 6 wt. % TiO_2 to about 9 wt. % TiO_2 , most preferably in the range from 7 to 8 wt. %. Conversion site burner flames 137 are formed with a fuel/oxygen mixture (Natural Gas and/or H_2 /with O_2) which combusts, oxidizes and converts the feedstocks at temperatures \geq about 1600° C. into soot 101 and consolidates the soot into glass 144. The temperatures of conduits 134 and the feedstocks contained therein are preferably controlled and monitored to inhibit reactions prior to flames 137 which may disrupt the flow of the feedstocks and soot 101 and complicate the manufacturing process of glass 144. Preferably furnace 140 and particularly zircon cup 142 and upper portion 138 are made from high purity zircon refractory bricks which are free of alkali and alkaline earth metals, and other impurities which tend to migrate from the furnace and contaminate glass 144. Such high purity bricks can be obtained using high purity ingredients and calcining the bricks to leach out impurities.

Preferably cup 142 has a circular diameter shape of at least 0.5 meters, more preferably at least 1 meter, so that glass body 144 is a cylindrical boule having a respective diameter of at least 0.5 meters, preferably 1 meter and a height of at least 8 cm, preferably at least 10 cm, with a preferred height in the range from 12 to 16 cm. Forming glass body 144 into a Ti doped high purity SiO_2 glass preform 60, preferably includes inspecting glass body 144 for any glass defects, such as inclusions, selecting defect free sections of glass body 144 such as defect free section 62 of glass body 144 shown in FIG. 7A, and

removing the defect free section 62 from 144 as shown in FIG. 7B. Preforms 60 are then formed from removed defect free section 62 as shown in FIG. 7C. Preform 60 is preferably obtained by core drilling section 62 from body 144 and cutting removed section 62 into planar preforms 60 having a top planar surface 64 and a bottom planar surface 66 of appropriate size that allows finishing of planar preforms 60 into a mask wafer with a planar mask wafer surface having a roughness ≤ 0.15 nm. As shown in FIG. 7E, planar preform 60 is finished by polishing a surface of preform 60. Preferably both the top surface 64 and the bottom surface 66 of preform 60 are polished with polishing wheels 68 to result in wafer 26. Preferably polishing preform 60 into wafer 26 includes at least two polishing steps. Preferably preform 60 is polished into a first polished preform surface having a roughness ranging from about 0.6 to 1.0 nm and then the polished preform surface is further polished into the planar mask wafer surface having a roughness ≤ 0.15 nm. Preferably polishing includes polishing with colloidal particles which are finely distributed particles in a liquid medium (solution) that do not settle out of solution. All of the particles are less than 0.5 microns in size and the entire size distribution of the particles is less than 0.5 microns and are preferably spherically shaped and have a diameter less than 100 nm, preferably in the range from 20 to 50 nm. Preferably the colloidal particles are comprised of silica, titania, alumina, or ceria. Most preferably the invention includes polishing with colloidal silica, preferably colloidal silica doped with titanium.

As shown in FIG. 7E, the polishing steps of the invention comprise polishing both the top surface 64 and the opposing bottom surface 66 concurrently. Preferably polishing wheels 68 include polishing wheel pads that are formed from synthetic polymers and the rotating motion of the synthetic polymer wheel surface and the polishing agent remove parts of the preform surfaces 64 and 66 through a combination of chemical and physical mechanical action to provide the finished smooth surface on which the reflective multilayer coating is deposited. Preferably the finishing includes polishing the preform surface with a cerium oxide abrasive and hard polyurethane pads, then polishing with a cerium oxide abrasive and soft

napped polyurethane pads, and then polishing with colloidal silica and soft polyurethane pads. In a preferred alternative embodiment the colloidal silica is doped with titanium. Preferably finishing includes polishing with an aqueous solution of at least one metal oxide and then polishing with an alkali aqueous solution of colloidal silica. After polishing with such polishing agents, the preform surfaces are cleaned to remove the polishing agents and provide cleaned wafer 26. Preferably the alkali aqueous solution of colloidal silica is buffered to a pH ranging from 8 to 12, preferably 10 to 12, and most preferably 10 to 11. The colloidal silica in the alkali aqueous solution through physical action removes any surface corrosion by the alkali solution and removes an continually-formed hydrated surface layer on the Ti doped SiO₂ glass.

The invention further includes reflective extreme ultraviolet soft x-ray mask wafer 26. The wafer comprises a Ti doped high purity SiO₂ inclusion-free glass wafer that has an unetched first polished planar face surface 32 and an opposing second polished planar face surface 31 with the first surface free of printable surface defects that have a dimension greater than 80 nm and having a roughness Ra ≤0.15 nm. FIG. 5A and 5B show such polished Ti doped high purity SiO₂ inclusion-free glass wafer surfaces. Preferably the second opposing surface 31 is also free of printable surface defects that have a dimension greater than 80 nm and has a roughness Ra ≤0.15 nm. Preferably the wafer has a thickness dimension between the first surface and the second surface of at least about 1mm, more preferably at least 5 mm, and most preferably in the ranges of 6 to 12 mm, and more preferably 6 to 8 mm. Preferably the Ti doped high purity SiO₂ inclusion free glass mask wafer is free of chlorine and has an impurity level of less than 10 ppb of the alkali and the alkaline earth metals.

The invention also includes a method of making a reflective extreme ultraviolet soft x-ray mask wafer that comprises providing a Ti doped high purity SiO₂ glass preform 60 having a first preform surface 64 and an opposing second preform surface 66 free of glass inclusions and finishing first preform surface 64 into a planar mask wafer surface having a roughness Ra ≤0.15 nm. Preferably providing a Ti doped high purity SiO₂ glass preform includes

providing a high purity Si containing feedstock 114 and high purity Ti containing feedstock 126, delivering feedstocks 114 and 126 to conversion site 100, converting feedstocks 114 and 126 into Ti doped SiO_2 soot 101, consolidating soot 101 into an inclusion free homogeneous Ti doped high 5 purity SiO_2 glass and forming the glass into a Ti doped high purity SiO_2 glass preform 60. Preferably Si feedstock 114 is chlorine free and Ti feedstock 126 is chlorine free so that soot 101 and glass 144 is chlorine free. Preferably finishing first preform surface 64 into planar mask wafer surface 32 includes polishing surface 64 into a first polished preform surface having a surface 10 roughness Ra ranging from about 0.6 to 1.0 nm and then further polishing the polished preform surface into a planar mask wafer surface having a roughness $\text{Ra} \leq 0.15$ nm. Preferably the polishing includes polishing with colloidal silica. In a preferred embodiment the colloidal silica is doped with titania in a 15 concentration of 4 to 10 wt.%. In a most preferred embodiment the method includes polishing opposing second preform surface 66 concurrently with polishing first surface 64. The finishing includes polishing the preform surface with an aqueous solution of at least one metal oxide and polishing the surfaces with an alkali solution of colloidal silica. Preferably the preform 60 surfaces are first polished with a cerium oxide abrasive and a hard polyurethane synthetic 20 polymer blown pad, then with a cerium oxide abrasive and a soft napped polyurethane synthetic polymer pad and then polished with colloidal silica and a soft polyurethane synthetic polymer pad. In a preferred embodiment the colloidal silica is doped with titanium. Preferably the preform has a thickness > 8mm and the preform is finished to provide a wafer 26 having a thickness > 6 25 mm. The finishing step in addition to polishing the preform surfaces with polishing agents, includes cleaning the preform surfaces to remove the polishing agents to provide a clean smooth surface for contact with the reflective multilayer coating. Preferably the step of providing a preform includes adjusting the Ti dopant weight percent level of the Ti doped high purity 30 SiO_2 glass preform so that the mask wafer has a coefficient of thermal expansion centered about 0 at an operating temperature of the reflective extreme ultraviolet soft x-ray mask.

The inventive method of making a reflective extreme ultraviolet soft x-ray mask wafer provides an economic means for efficiently manufacturing large quantities of mask wafers which can enable the utilization of extreme ultraviolet soft x-ray projection lithography for the mass production of integrated circuits with printed feature dimensions less than 100 nm. Additionally, the inventive method of making a reflective mask Ti doped high purity SiO_2 glass wafer 26 includes the beneficial steps of inspecting and qualifying the finished top wafer surface 32 to ensure that the surface has proper roughness and is defect free, and additionally inspecting and qualifying the finished bottom wafer surface 31 to determine the roughness and defect free qualifications of the opposing second glass wafer surface. Preferably, AFM is used in such inspection and qualification. This improves the selection and yield of mask wafers for coating and further utilizing as part of the mask system for extreme ultraviolet soft x-ray projection lithography.

During use of the patterned reflective extreme ultraviolet soft x-ray mask, the Ti doped SiO_2 glass wafer has a lithography operating temperature. The operating temperature of the SiO_2 glass wafer include a maximum operating temperature. During the making of the patterned reflective extreme ultraviolet soft x-ray mask, the Ti doped SiO_2 glass is exposed to manufacturing treatment temperatures. The manufacturing treatment temperatures include elevated temperatures during cutting, machining, finishing, and coating. The manufacturing treatment temperatures include a maximum manufacturing temperature. Preferably the Ti doped SiO_2 glass wafer is crystallization resistant and has a crystallization temperature T_{crystal} at which crystallization in the glass is induced, with T_{crystal} being substantially greater than the maximum operating temperature and the maximum manufacturing temperature. Preferably T_{crystal} is at least 400° C. higher than the greater of the maximum operating temperature and the maximum manufacturing temperature, more preferably at least 700° C. higher, and most preferably at least 800° C higher. Preferably T_{crystal} is $\geq 1300^{\circ}$ C., with the maximum operating temperature and maximum manufacturing temperature not exceeding 500° C. With the Ti doped SiO_2 glass, the crystallization of the

glass is inhibited at the elevated temperatures experienced during manufacturing and use of the mask. The glass wafer is thus beneficial in that it has a high temperature crystallization behavior.

Additionally, in view of the manufacturing treatment temperatures and the lithography operating temperatures, the Ti doped SiO_2 glass maintains its physical dimensions when exposed to thermal cycling. In the use and manufacture of the Ti doped SiO_2 glass wafer reflective mask repeated thermal cycling between lower temperatures and higher temperatures do not substantially change the physical dimensions of the glass wafer. Preferably the Ti doped SiO_2 glass is resistant to thermal cycling hysteresis, and most preferably is free of thermal cycling hysteresis when repeatedly cycled (> 100 cycles) from the lowest lithography operating temperature to the highest lithography operating temperature. Most preferably physical dimensions of the glass wafer do not measurably change when repeatedly cycled from a low temperature to a high temperature that are below 300° C. with the low temperature proximate to 0° C. and the high temperature proximate to 300° C.

In a preferred embodiment, the Ti doped SiO_2 glass wafer has a birefringence resulting from permanent strain in the glass of less than 10 nm/cm, and more preferably is less than 2 nm/cm. Preferably birefringence less than 2 nm/cm is achieved by providing a constant homogeneous distribution of Ti dopant throughout the SiO_2 glass with the coefficient of thermal expansion in the range from + 10 ppb to - 10 ppb at 20° C., with the maximum fluctuation in the coefficient of thermal expansion being less than 10 ppb, most preferably less than 5 ppb. Additionally, low birefringence may be obtained by annealing the Ti doped SiO_2 glass. Preferably the Ti doped SiO_2 glass is annealed at a temperature of at least 900° C., more preferably at least 1000° C., and most preferably at least 1030° C. after the glass has experienced stress, such as from machining.

Ensuring such low birefringence levels is preferably achieved by monitoring the coefficient of thermal expansion of the Ti doped SiO_2 glass by transmitting ultrasonic waves through the glass and measuring the transit time of the ultrasonic waves through the glass to determine the ultrasonic velocity

and expansivity characteristics of the glass exposed to the ultrasonic waves. Preferably such measurement and monitoring of the Ti doped SiO_2 glass is utilized throughout the mask manufacturing process. Such ultrasonic measurements are preferably utilized for quality control, inspection, and selection. Such measurement are preferably utilized to insure the manufacturing of glass body 144 is providing a constant homogeneous distribution of Ti dopant. Additionally, such measurements are used in inspecting glass body 144 and selecting sections 62 to be cut from glass body 144. Further such measurements may be utilized to insure that undue stresses are not formed in the glass during later manufacturing stages such as finishing and also used as a determining factor if further annealing of the glass is required.

EXAMPLE

FIG. 5A-5B are AFM photomicrographs of a Ti doped high purity SiO_2 glass wafer surface. FIG. 5A-5B were taken at two separate locations on the same finished wafer sample top surface. The mask wafer sample was obtained by finishing a Ti doped high purity SiO_2 glass preform that was free of inclusions. The preform had a square shape with approximately 7.6 cm sides and a thickness of approximately 0.64 cm. The square preform was obtained by cutting the preform from an inclusion free area of a Ti doped high purity SiO_2 glass boule that had a cylindrical shape of approximately 152 cm diameter and a 14 cm thickness (height). The boule was produced from octamethylcyclotetrasiloxane and titanium isopropoxide feedstocks in accordance with FIG. 6 of the invention with the Ti doped high purity SiO_2 glass having a TiO_2 wt. % of about 7.5 wt. %. The square preform were finished into a planar mask wafer using a double sided lapping/polishing machine. The square preform was first lapped using 7 micron alumina abrasives on cast iron plate to remove approximately 20/1000 inches (0.0508 cm) of the preform thickness. Then it was polished on blown synthetic polyurethane hard pad (Rodel Inc., 3804 E. Watkins Street, Phoenix, AZ; Rodel MHC-14B brand blown polyurethane pad) for one hour using cerium oxide (Rodia Inc., 3

Enterprise Drive, Shelton, Conn.; Rodia (Rhone-Poulence) Opaline brand cerium oxide) at 1.5 psi (0.1055 kg/cm²) and 50 RPMs. The preform was then polished on napped synthetic polyurethane soft pad (Rodel Inc., Rodel 204 Pad brand napped polyurethane pad) for twenty minutes using cerium oxide
5 (Universal Photonics Inc., 495 W. John Street, Hicksville, NY; Universal Photonics Hastelite 919 brand cerium oxide) at 1.5 psi (0.1055 kg/cm²) and 50 RPMs. The preform was then polished on napped synthetic polyurethane soft pad (Rodel 204 Pad brand napped polyurethane pad) for five to ten minutes using colloidal silica (Cabot Corp., 75 State Street, Boston, MA; Cabot A2095
10 brand colloidal silica) at 1.5 psi (0.1055 kg/cm²) and 50 RPMs. The resulting planar mask wafer was then analyzed, measured, and qualified using AFM with the resulting surface of FIG. 5A-5B. The finished Ti doped SiO₂ glass wafer surface has an Ra roughness \leq 0.15 nm. In a preferred embodiment the Ti doped SiO₂ glass wafer surface of the invention is defect free with an Ra
15 roughness \leq 0.10 nm, more preferably Ra roughness \leq 0.09 nm, and most preferably Ra roughness \leq 0.086 nm. Additionally, the Ti doped SiO₂ glass wafer surface preferably has an RMS roughness \leq 0.15 nm, and average height \leq 0.5 nm with a maximum range \leq 0.9 nm.

20 It will be apparent to those skilled in the art that various modifications and variations can be made to the present invention without departing from the spirit and scope of the invention. Thus, it is intended that the present invention cover the modifications and variations of this invention provided they come within the scope of the appended claims and their equivalents.

WE CLAIM:

1. A projection lithographic method for producing integrated circuits with printed feature dimensions less than 100 nm, said method comprising:
 - 5 providing an illumination sub-system for producing and directing an extreme ultraviolet soft x-ray radiation λ , said illumination sub-system including an extreme ultraviolet soft x-ray source,
 - providing a mask sub-system illuminated by said extreme ultraviolet soft x-ray radiation λ produced by said illumination sub-system,
 - 10 providing a mask sub-system patterned reflective lithographic mask for forming a projected mask lithographic pattern when illuminated by said radiation λ , said patterned reflective mask including a Ti doped SiO₂ glass wafer with a patterned λ absorbing overlay overlaying a λ reflective multilayer coated Ti doped SiO₂ glass defect-free wafer surface that has an Ra roughness ≤ 0.15 nm,
 - 15 providing a projection sub-system,
 - providing an integrated circuit wafer, said integrated circuit wafer having a λ radiation sensitive wafer surface, and
 - projecting with said projection sub-system the projected mask pattern from said patterned reflective mask onto said radiation sensitive wafer surface.
2. A method as claimed in claim 1 wherein said Ti doped SiO₂ glass wafer is inclusion free.
- 25 3. A method as claimed in claim 1 wherein said Ti doped SiO₂ glass wafer surface is an unetched glass surface.
4. A method as claimed in claim 1 wherein said Ti doped SiO₂ glass wafer is substantially non-transmissive to said extreme ultraviolet soft x-ray radiation
- 30 λ .

5. A method as claimed in claim 1 wherein said Ti doped SiO₂ glass wafer is chlorine-free.
- 5 6. A method as claimed in claim 1 wherein said Ti doped SiO₂ glass wafer has an impurity level of less than 10 ppm Cl, less than 10 ppm of alkali and alkaline earth metals.
- 10 7. A method as claimed in claim 1 wherein said Ti doped SiO₂ glass wafer contains from 5 to 10 wt. % TiO₂ and has a coefficient of thermal expansion in the range from + 30 ppb to – 30 ppb at 20° C.
8. A method as claimed in claim 7 wherein said coefficient is in the range of + 10 ppb to –10 ppb at 20° C.
- 15 9. A method as claimed in claim 8 wherein said wafer has a variation in coefficient of thermal expansion \leq 15 ppb.
- 10 10. A method as claimed in claim 1 wherein said wafer has a thermal conductivity K \leq 1.40 w/(m x °C) at 25°C.
- 20 11. A method as claimed in claim 1 wherein said Ti doped SiO₂ glass wafer is heated by said radiation λ , and said patterned absorbing overlay is substantially unaffected by said heating of the glass wafer.
- 25 12. A method as claimed in claim 2 wherein said Ti doped SiO₂ glass defect-free wafer surface has a finished planar surface.
13. A method as claimed in claim 12 wherein said finished planar surface is free of printable surface imperfections that have a dimension greater than 80 30 nm .

14. A method as claimed in claim 12 wherein said finished planar surface is free of printable surface imperfections that have a dimension greater than 1/5 the printed feature dimension.

5 15. A method as claimed in claim 7, wherein said Ti doped SiO_2 glass wafer is heated to an operating temperature by said radiation λ and said Ti doped SiO_2 glass wafer has a Ti dopant level such that said glass wafer has a coefficient of thermal expansion at said operating temperature centered about 0.

10 16. A method as claimed in claim 1 wherein said reflective multilayer coated Ti doped SiO_2 glass wafer surface reflects at least 65% of the radiation λ illuminating the reflective multilayer coated Ti doped.

15 17. A method as claimed in claim 16 wherein said reflective multilayer coated Ti doped SiO_2 glass wafer surface reflects at least 70% of the radiation λ .

20 18. A method as claimed in claim 16 wherein said reflective multilayer is directly coated on said Ti doped SiO_2 glass wafer surface.

19. A method as claimed in claim 1 wherein said Ti doped SiO_2 glass wafer has an OH content less than 1200 ppm.

25 20. A method as claimed in claim 1 wherein said reflective lithographic mask elements has a maximum operating temperature and a maximum manufacturing temperature and said Ti doped high purity SiO_2 glass has a crystallization temperature T_{crystal} which is greater than said maximum operating temperature and said maximum manufacturing temperature.

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21. A method as claimed in claim 20 wherein T_{crystal} is at least 400°C higher than said maximum operating temperature and said maximum manufacturing temperature.

5 22. A method as claimed in claim 21 wherein $T_{\text{crystal}} > 1100^{\circ}\text{C}$

23. A method as claimed in claim 1 wherein said reflective lithographic mask has a lowest lithography operating temperature and a highest lithography operating temperature, said Ti doped high purity SiO_2 glass free of thermal 10 cycling hysteresis when repeatedly cycled at least a hundred times from said lowest lithography operating temperature to said highest lithography operating temperature.

15 24. A method as claimed in claim 1 wherein said Ti doped high purity SiO_2 glass has a birefringence less than 10 nm/cm.

25. A method of making a projection lithographic printing pattern, said method comprising:

20 providing an illumination sub-system including an extreme ultraviolet soft x-ray source,

providing a mask sub-system, said mask sub-system including a mask receiving member and a reflective mask Ti doped high purity SiO_2 glass mask wafer with an unetched glass mask wafer surface coated with a reflective multilayer coating having a reflectivity of at least 65% to extreme ultraviolet soft 25 x-rays, said reflective mask received by said mask receiving member,

providing a projection sub-system, said projection sub-system including a camera with a depth of focus $\geq 1 \mu\text{m}$ and a numerical aperture $\text{NA} \leq 0.1$,

providing a radiation sensitive print sub-system, said radiation sensitive print sub-system including a radiation sensitive print media,

30 aligning said illumination sub-system, said mask sub-system, said projection sub-system, and said radiation sensitive print sub-system wherein said extreme ultraviolet soft x-ray source illuminates said reflective mask with

extreme ultraviolet soft x-ray radiation, said reflective mask reflects said radiation and forms a printing pattern which is projected by said projection sub-system camera onto said radiation sensitive print media.

- 5 26. A method as claimed in claim 25, wherein providing a mask sub-system including a reflective mask, includes providing a reflective mask with a wafer surface that has an Ra roughness ≤ 0.15 nm.
- 10 27. A method as claimed in claim 25, wherein providing a mask sub-system including a reflective mask, includes providing a reflective mask Ti doped high purity SiO_2 glass mask wafer free of wafer surface defects that are printable on said radiation sensitive print media.
- 15 28. A method as claimed in claim 25, wherein providing a mask sub-system including a reflective mask, includes providing a Ti doped high purity SiO_2 glass mask wafer preform having a preform surface and free of inclusions and finishing said preform surface into a planar mask wafer surface having an Ra roughness ≤ 0.15 nm.
- 20 29. A method as claimed in claim 25, further comprising, determining an operating temperature of said reflective mask when illuminated by said illumination sub-system during operation of said projection lithographic sub-systems, and providing a mask sub-system with a reflective mask includes providing a Ti doped high purity SiO_2 glass mask wafer having a coefficient of thermal expansion at said reflective mask operating temperature centered about 0.
- 25 30. A method as claimed in claim 25, wherein said Ti doped high purity SiO_2 glass mask wafer is heated to a raised temperature range by said extreme ultraviolet soft x-ray radiation and said Ti doped high purity SiO_2 glass has a coefficient of thermal expansion for said raised temperature range that is less than 10 ppb and greater than -10 ppb.

31. A method as claimed in claim 30 wherein said glass has a variation in coefficient of thermal expansion ≤ 10 ppb.
- 5 32. A method as claimed in claim 25 wherein said Ti doped high purity SiO_2 glass mask has a thermal conductivity $K \leq 1.40$ w/(m x $^{\circ}\text{C}$) at 25°C .
- 10 33. A method of making a reflective extreme ultraviolet soft x-ray mask, said method comprising:
 - 10 providing a Ti doped high purity SiO_2 glass preform having a preform surface and free of inclusions,
 - finishing said preform surface into a planar mask wafer surface having an Ra roughness ≤ 0.15 nm,
 - coating said finished planar mask wafer surface having an Ra
 - 15 roughness ≤ 0.15 nm with a reflective multilayer coating to form a reflective mask surface having a reflectivity of at least 65% to extreme ultraviolet soft x-rays.
- 20 34. A method as claimed in claim 33, further comprising forming a patterned absorbing overlay on said reflective mask surface.
- 25 35. A method as claimed in claim 33, wherein providing a Ti doped high purity SiO_2 glass preform further includes, providing a high purity Si containing feedstock and a high purity Ti containing feedstock, delivering said high purity Si containing feedstock and said high purity Ti containing feedstock to a conversion site, converting said Si containing feedstock and said Ti containing feedstock into Ti doped SiO_2 soot, consolidating said Ti doped SiO_2 soot into an inclusion free homogeneous Ti doped high purity SiO_2 glass, forming said glass into a Ti doped high purity SiO_2 glass preform.
- 30 36. A method as claimed in claim 35, wherein providing a high purity Si containing feedstock and a high purity Ti containing feedstock includes

providing a chlorine-free high purity Si containing feedstock and providing a chlorine-free high purity Ti containing feedstock, converting said chlorine-free feedstocks into chlorine-free Ti doped SiO_2 soot, and consolidating said soot into a chlorine-free Ti doped SiO_2 glass.

5

37. A method as claimed in claim 33 wherein finishing said preform surface into a planar mask wafer surface further comprises polishing said preform surface into a first polished preform surface having a surface Ra roughness ranging from about 0.6 to 1.0 nm, and further polishing said polished preform surface into said planar mask wafer surface having an Ra roughness ≤ 0.15 nm.

10

38. A method as claimed in claim 37 wherein further polishing includes polishing with colloidal particles.

15

39. A method as claimed in claim 33 wherein said preform has an opposing second preform surface and finishing said preform surface further comprising second preform surface concurrently with polishing said preform surface.

20

40. A method as claimed in claim 33 wherein finishing further includes polishing said preform surface with a synthetic polymer pad.

25

41. A method as claimed in claim 39 wherein finishing further includes polishing said preform surface with a cerium oxide abrasive and a hard polyurethane pad, then polishing with a cerium oxide abrasive and a soft polyurethane pad, and then polishing with a colloidal silica and a soft polyurethane pad.

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42. A method as claimed in claim 33 wherein finishing further includes polishing said preform surface with an aqueous solution of at least one metal oxide and then polishing said preform surface with an alkali aqueous solution of colloidal silica.

43. A method as claimed in claim 33 wherein finishing said preform surface includes polishing said preform surface with polishing agents and then cleaning said preform surface to remove said polishing agents.

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44. A method as claimed in claim 33, wherein said glass preform has a Ti dopant weight percent level and providing a Ti doped high purity SiO_2 glass preform further includes adjusting said Ti dopant weight percent level so that said mask has a coefficient of thermal expansion centered about 0 at an 10 operating temperature of said mask.

45. A reflective extreme ultraviolet soft x-ray mask wafer comprising:
a Ti doped high purity SiO_2 inclusion-free glass wafer having an unetched first polished face surface and an opposing second polished planar 15 face surface, said first surface free of printable surface defects that have a dimension greater than 80 nm and having an Ra roughness ≤ 0.15 nm.

46. A mask wafer as claimed in claim 45 wherein said glass is crystal-free.

20 47. A mask wafer as claimed in claim 45 wherein said glass wafer consists of silicon, titanium, and oxygen.

48. A mask wafer as claimed in claim 45 wherein said wafer has a thickness dimension between said first face surface and said second face surface of at 25 least about 1cm.

49. A mask wafer as claimed in claim 45 wherein said glass wafer is free of chlorine.

30 50. A mask wafer as claimed in claim 45 wherein said glass wafer has an impurity level of less than 10 ppm of alkali and alkaline earth metals.

51. A method of making a reflective extreme ultraviolet soft x-ray mask wafer, said method comprising:

providing a Ti doped SiO_2 glass preform and an opposing second preform surface and free of inclusions,

5 finishing said first preform surface into a planar mask wafer surface having an Ra roughness ≤ 0.15 nm.

52. A method as claimed in claim 51 wherein said mask wafer is heated to a raised temperature range by extreme ultraviolet soft x-ray radiation and said Ti doped SiO_2 glass has a coefficient of thermal expansion for said raised 10 temperature range that is less than 10 ppb and greater than -10 ppb and has a variation ≤ 10 ppb.

53. A method as claimed in claim 51, wherein providing a Ti doped SiO_2 glass preform further includes, providing a high purity Si containing feedstock and a high purity Ti containing feedstock, delivering said high purity Si containing feedstock and said high purity Ti containing feedstock to a conversion site, converting said Si containing feedstock and said Ti containing feedstock into Ti doped SiO_2 soot, consolidating said Ti doped SiO_2 soot into 20 an inclusion free homogeneous Ti doped high purity SiO_2 glass body, forming said glass body into a Ti doped high purity SiO_2 glass preform.

54. A method as claimed in claim 53, wherein providing a high purity Si containing feedstock includes providing a chlorine-free high purity Si containing feedstock and providing a chlorine-free high purity Ti containing feedstock, 25 converting said chlorine-free feedstocks into chlorine-free Ti doped SiO_2 soot, and consolidating said soot into chlorine-free Ti doped SiO_2 glass.

55. A method as claimed in claim 51 wherein finishing said first preform 30 surface into a planar mask wafer surface further comprises polishing said first preform surface into a first polished preform surface having a surface Ra roughness ranging from about 0.6 to 1.0 nm, and further polishing said

polished preform surface into said planar mask wafer surface having an Ra roughness ≤ 0.15 nm.

5 56. A method as claimed in claim 55 wherein further polishing includes polishing with colloidal silica.

57. A method as claimed in claim 51 wherein finishing said first preform surface further comprises polishing said opposing second preform surface concurrently with polishing said first preform surface.

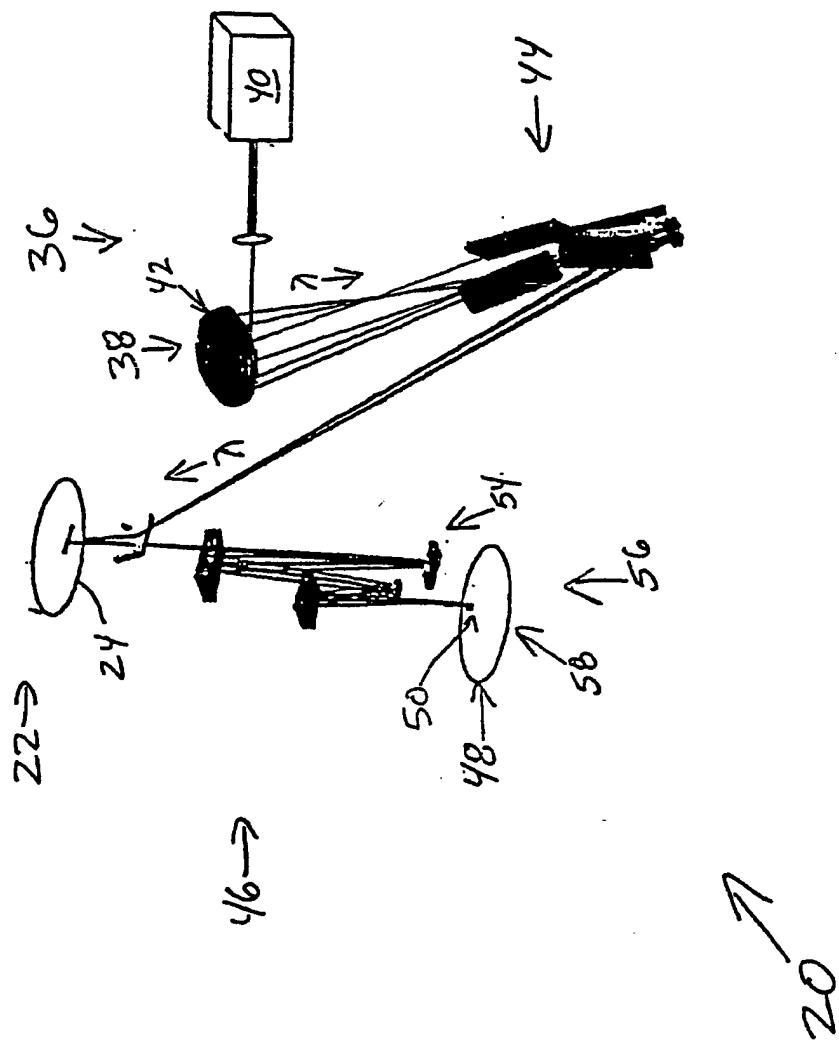
10 58. A method as claimed in claim 51 wherein finishing further includes polishing said first preform surface with a cerium oxide abrasive and a hard polyurethane pad, then polishing with a cerium oxide abrasive and a soft polyurethane pad, and then polishing with a colloidal silica and a polyurethane 15 pad.

20 59. A method as claimed in claim 51 wherein finishing further includes polishing said preform surface with an aqueous solution of at least one metal oxide and then polishing said preform surface with an alkali aqueous solution of colloidal silica.

25 60. A method as claimed in claim 51 wherein finishing said first preform surface includes polishing said first preform surface with polishing agents and then cleaning said first preform surface to remove said polishing agents .

30 61. A method as claimed in claim 51, wherein said glass preform has a Ti dopant weight percent level and providing a Ti doped SiO_2 glass preform further includes adjusting said Ti dopant weight percent level so that said mask has a coefficient of thermal expansion centered about 0 at an operating temperature of said mask.

62. A method as claimed in claim 51, wherein said glass preform consists of silicon, titanium, and oxygen.



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2/9

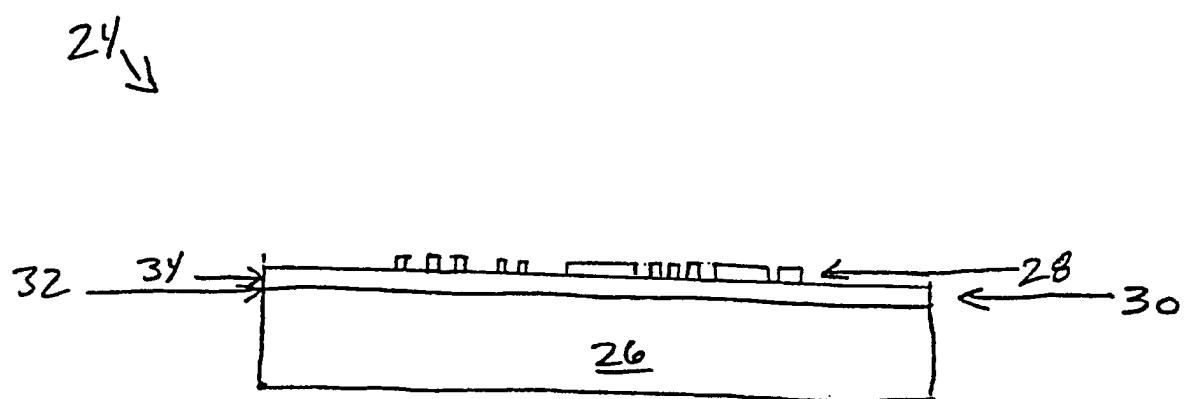


FIG.2

3/9

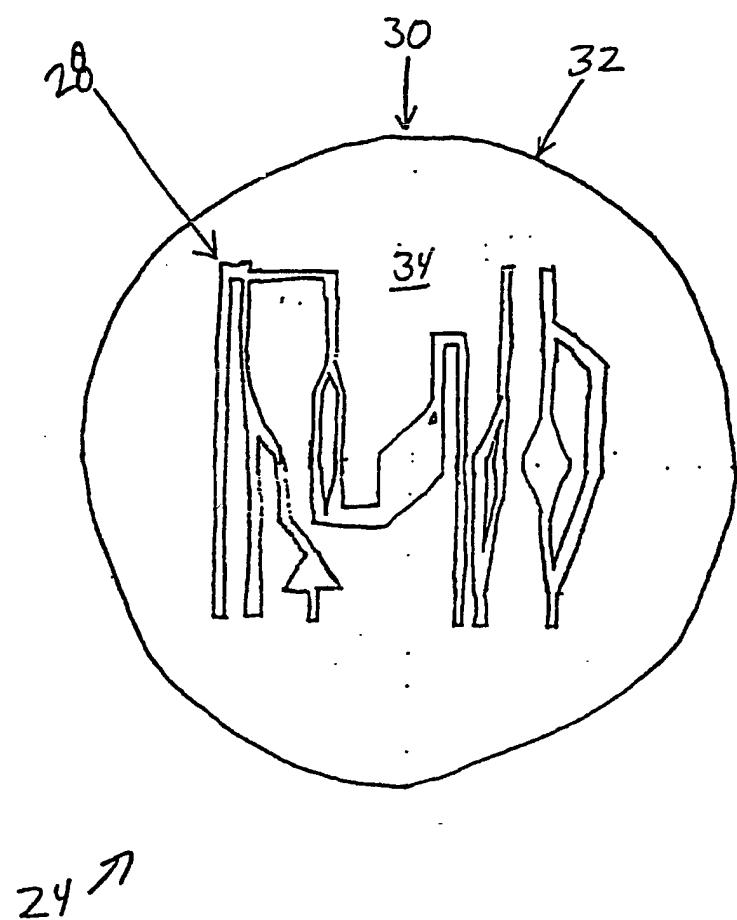
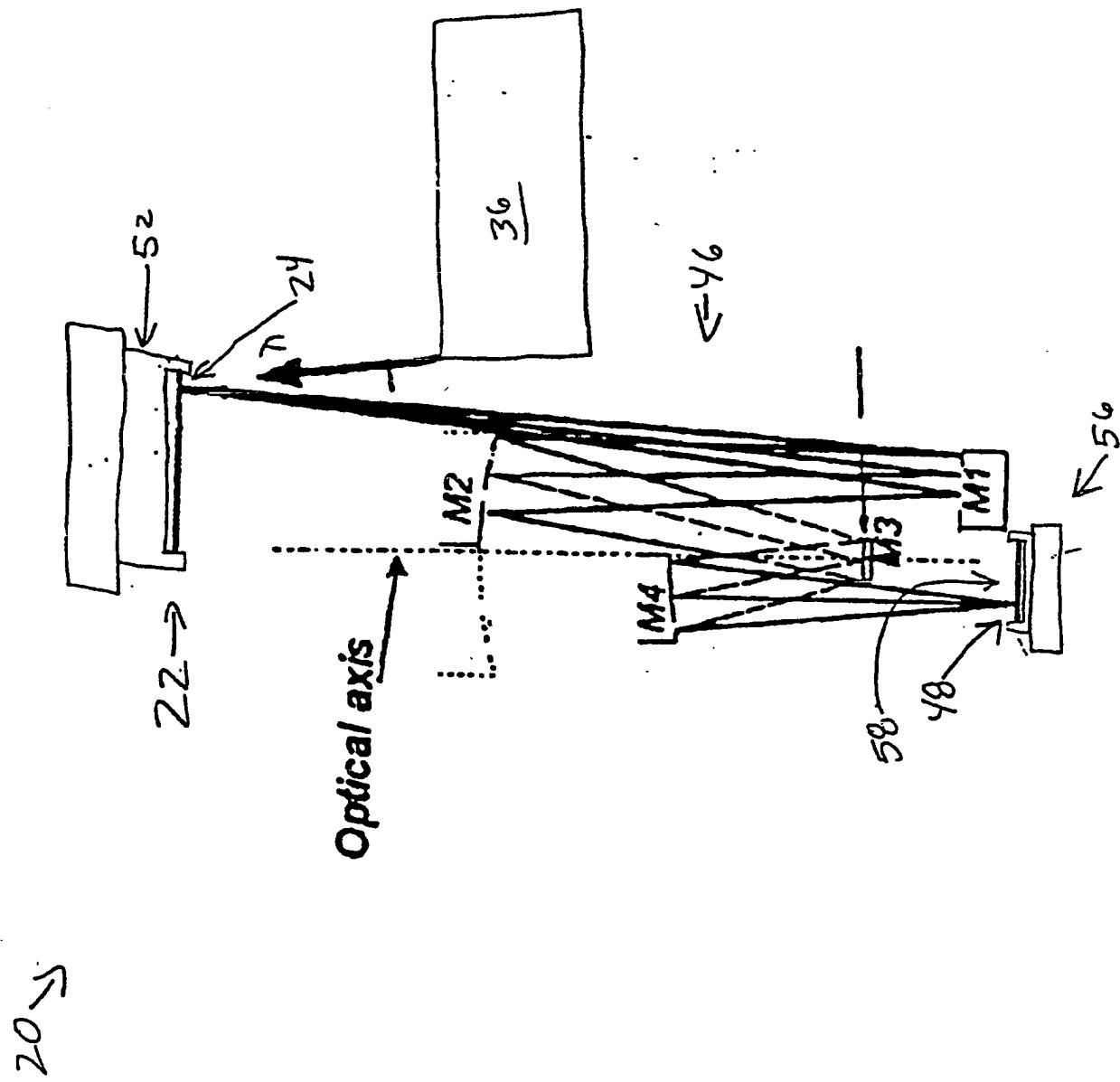


FIG 3.

FIG. 4



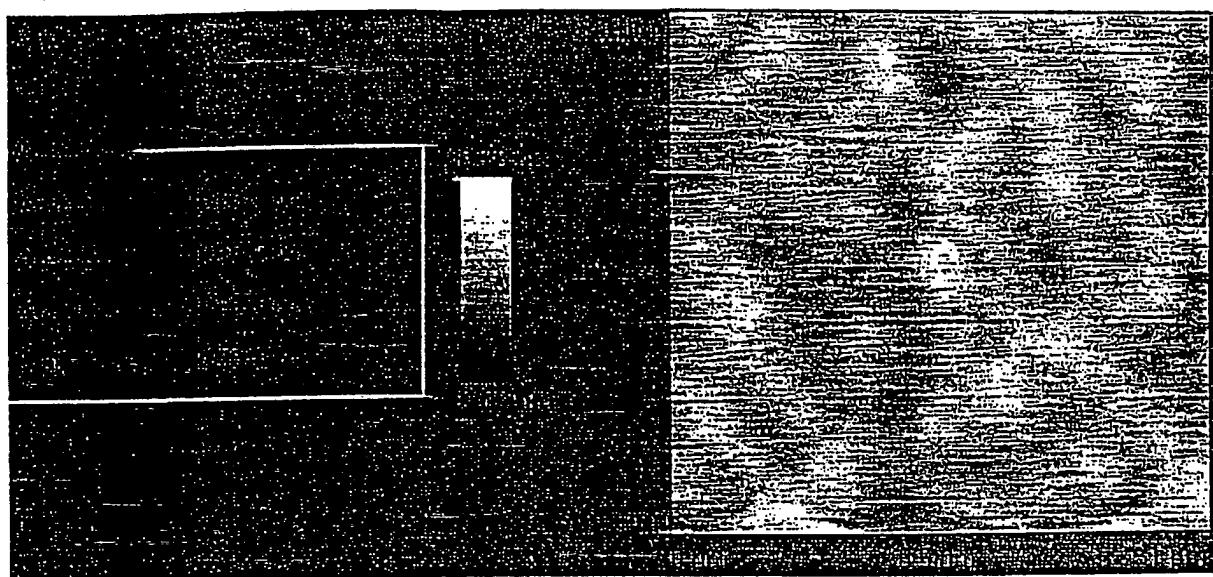


FIG. 5A

6/9

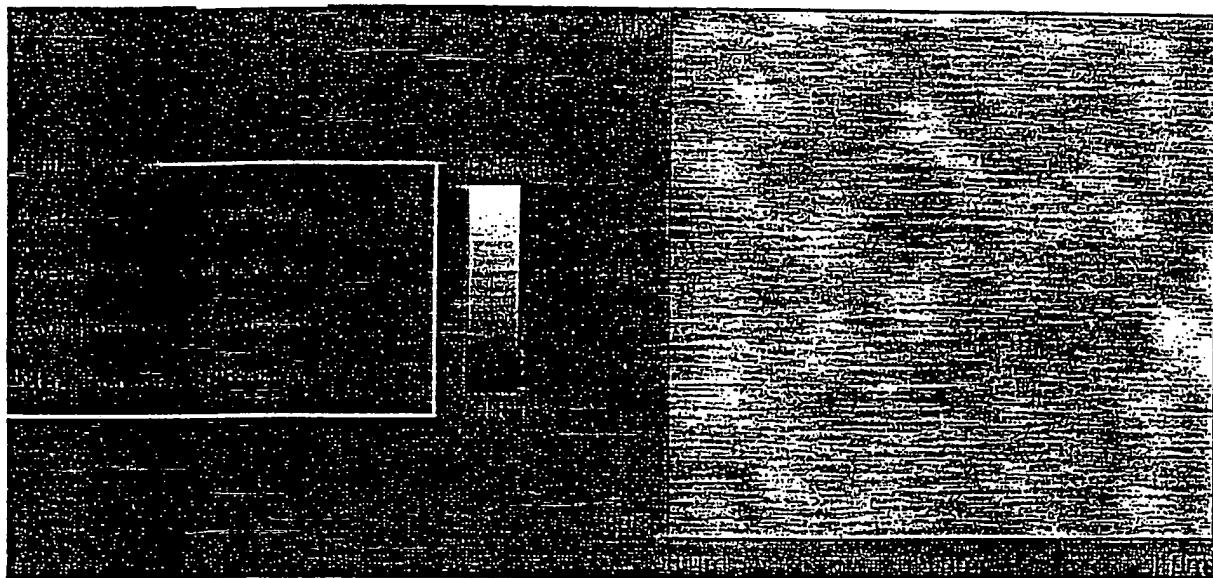


FIG.5B

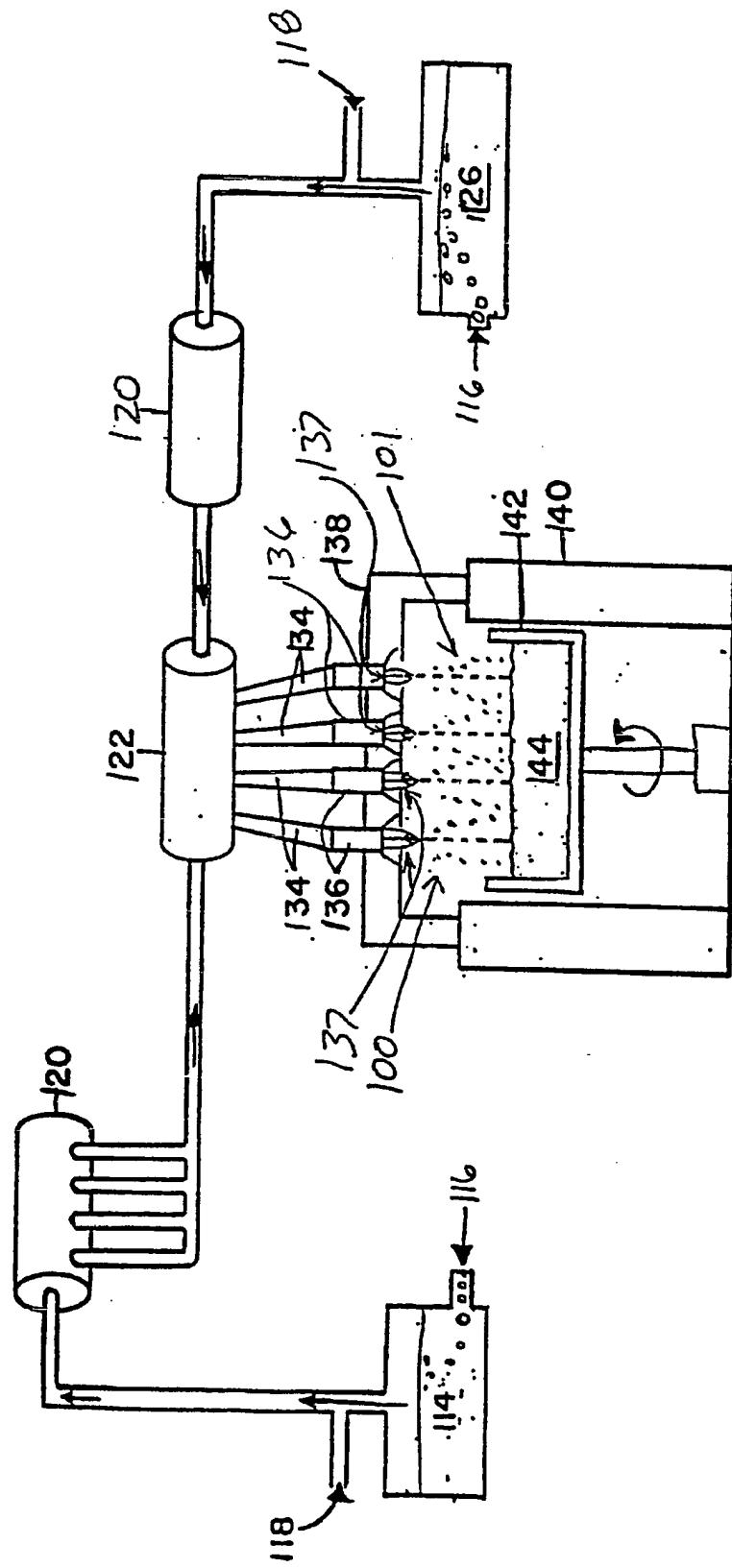


FIG. 6

8/9

FIG. 7A



FIG. 7B

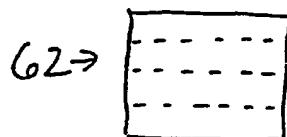


FIG. 7C

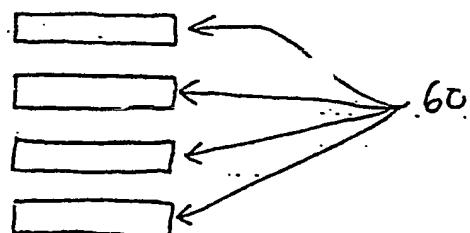


FIG. 7D

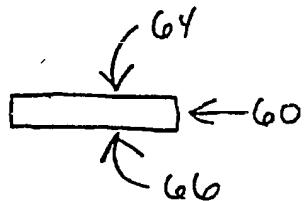


FIG. 7E

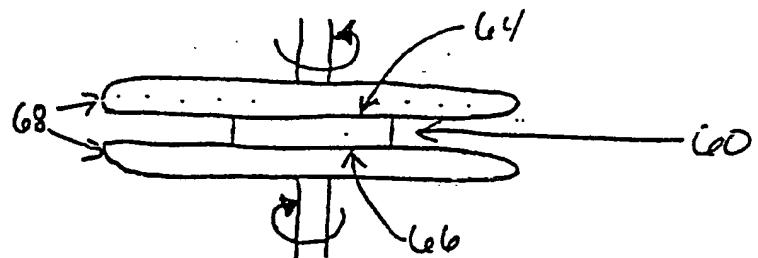
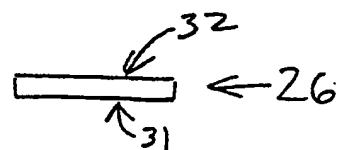


FIG. 7F



9/9

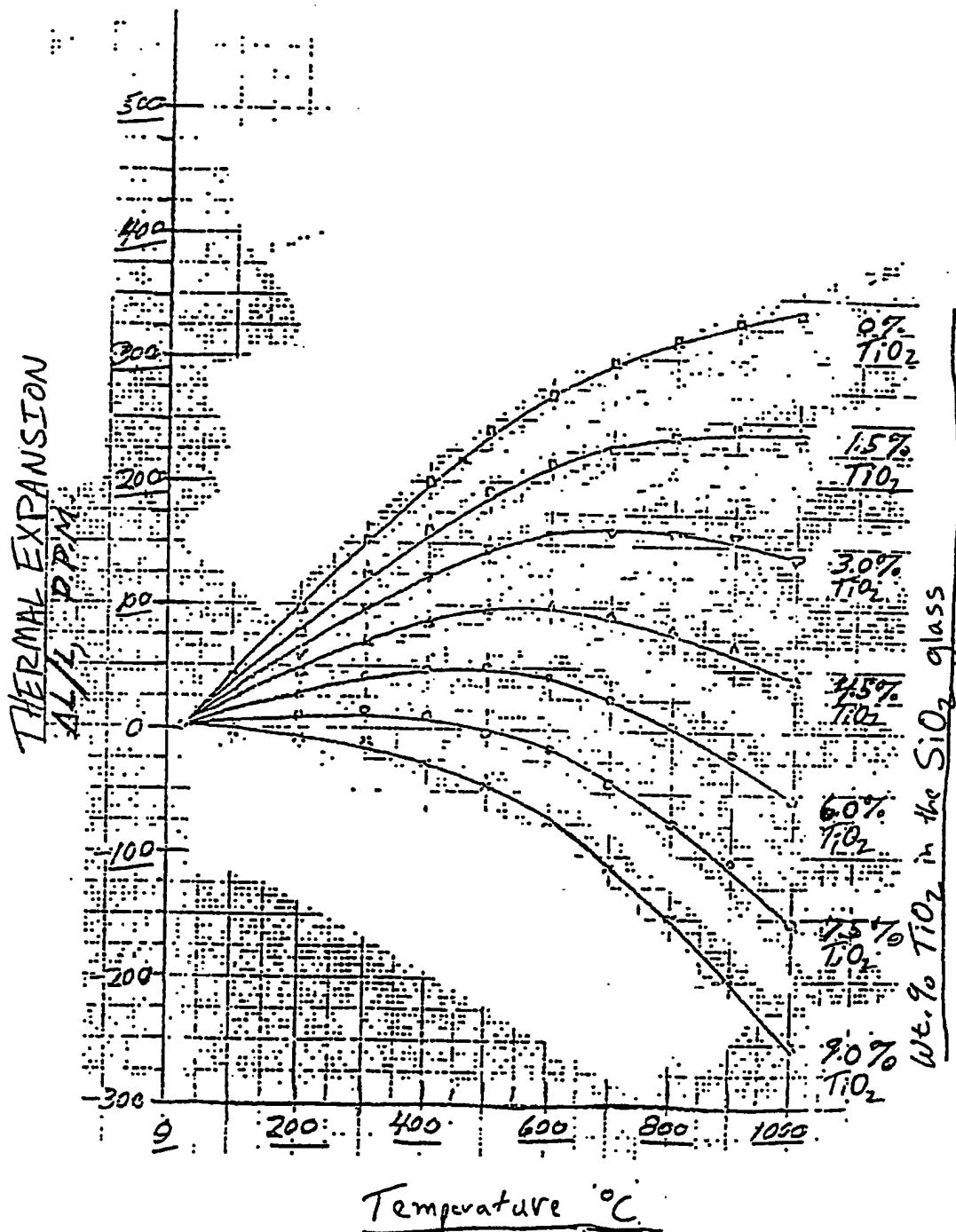
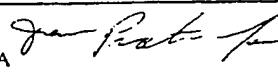


FIG. 8

INTERNATIONAL SEARCH REPORT

International application No.

PCT/US00/19060

A. CLASSIFICATION OF SUBJECT MATTER		
IPC(7) : G03C 5/00 US CL : 430/311		
According to International Patent Classification (IPC) or to both national classification and IPC		
B. FIELDS SEARCHED		
Minimum documentation searched (classification system followed by classification symbols) U.S. : 430/5, 311, 313, 318; 438/423, 424, 723		
Documentation searched other than minimum documentation to the extent that such documents are included in the fields searched		
Electronic data base consulted during the international search (name of data base and, where practicable, search terms used) USPAT		
C. DOCUMENTS CONSIDERED TO BE RELEVANT		
Category *	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.
Y, P	US 5,989,776 A (FELTER et al.) 23 November 1999 (23.11.1999), column 2, lines 35-50	1-62
Y	US 5,565,030 A (KADO et al.) 15 October 1996 (15.10.1996), column 1, line 60 - column 2, line 40.	1-62
Y	US 5,395,738 A (BRANDES et al.) 07 March 1995 (7.03.1995), column 2, line 60 - column 10, line 10.	1-62
Y	US 5,553,110 A (SENTOKU et al.) 03 September 1996 (3.09.1996), column 5, line 20 - column 27, line 55.	1-62
Y	US 5,868,605 A (CESNA) 09 February 1999 (9.02.1999), column 1, line 10 - column 2, line 55.	1-62
Y	US 5,605,490 A (LAFFEY et al.) 25 February 1997 (25.02.1997), column 1, line 60 - column 3, line 25.	1-62
Y	US 5,146,518 A (MAK et al.) 08 September 1992 (8.09.1992), column 1, line 5 - column 5, line 45.	1-62
Y	US 4,002,512 A (LIM) 11 January 1977 (11.01.1977), column 1, line 10 - column 3, line 8.	1-62
Y	US 4,911,513 A (VALETTE) 27 March 1990 (27.03.1990), column 4, line 40 - column 8, line 45.	1-62
<input type="checkbox"/> Further documents are listed in the continuation of Box C. <input type="checkbox"/>		See patent family annex.
<ul style="list-style-type: none"> • Special categories of cited documents: 		
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Name and mailing address of the ISA/US Commissioner of Patents and Trademarks Box PCT Washington, D.C. 20231 Facsimile No. (703)305-3230	Authorized officer SALEHA R. MOHAMEDU  Telephone No. (703) 308-1161	